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# Wafer-scale bonded GaN–AIN with high interface thermal conductance

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# ABSTRACT

Wide and ultrawide bandgap semiconductors, such as GaN, play a crucial role in high-power applications, yet their performance is often constrained by thermal management challenges. In this work, we introduce a high-quality interface between GaN and AlN, prepared through wafer-scale bonding and verified via high-resolution transmission electron microscopy and transport experiments. We experimentally measured the thermal boundary conductance of the GaN–AlN interface, achieving up to 320 MW/m<sup>2</sup>K at room temperature using an ultrafast optical technique and sensitivity examinations. Non-equilibrium atomistic Green's functions and density functional theory simulations were conducted to model the interface phonon modes and their contributions to thermal transport, demonstrating good agreement with the experimental results from 80 to 300 K. Additionally, we observed a size-dependent effect on the thermal boundary conductance related to the GaN film thickness from 180 to 450 nm, which we attributed to quasi-ballistic thermal transport through molecular dynamics simulations. Our study has demonstrated a scalable processing route for wafer-sized chip packaging and provides fundamental insights to mitigate nearjunction thermal resistance. Further exploration of interface engineering could facilitate co-design strategies to advanced thermal management technologies.

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Wide and ultrawide bandgap semiconductor materials play a critical role in emerging technologies from power electronics, optoelectronics, photonics, to quantum devices.<sup>1-3</sup> However, thermal management of high-power devices represents one of the most formidable challenges.4-10 The demand for heat dissipation in gallium nitride (GaN)-based devices has been escalating as the power density is anticipated to approach nearly 1 MW/cm<sup>2</sup> by 2030 and posed a considerable impact on device performance, stability, and lifetime.<sup>10</sup> In particular, interfacial heat transfer between adjacent materials layers, characterized by its thermal boundary conductance (TBC), has been recognized as a major contribution to the total thermal resistances that restrict heat dissipation.<sup>4</sup> In this study, we performed the experimental measurements of TBC at the interfaces between GaN and aluminum nitride (AlN) in wafer-size bonded samples. The experimental structure was systematically analyzed with measurement sensitivity and atomistic modeling to provide reliable determination of a high interface thermal conductance up to 320 MW/m<sup>2</sup>K. The interface quality of the bonded GaN-AlN was also examined through transmission

electron microscopy imaging and temperature-dependent measurements of TBC using time-domain thermoreflectance (TDTR). We observed a size effect in TBC from the measurements and attributed it to quasi-ballistic thermal transport in the confined GaN layer. The contributions to TBC from spectral-dependent phonon transmission were revealed using atomic Green's function with density functional theory (DFT) and molecular dynamics (MD) simulations. The study provides fundamental insights and future co-design guideline for thermal management of wide and ultrawide bandgap semiconductors.

Fundamentally, TBC of semiconductor interfaces arises from multi-scale contributions including atomistic interface scattering, incomplete microscale contacts, and disorders near the interface, which have been thoroughly discussed in the recent review.<sup>4</sup> For materials grown under optimized conditions, the TBC limit is defined by the breakdown of coherent phonon transport at heterogeneous junctions due to phonon spectral mismatching.<sup>4</sup> The interface between AlN and GaN is especially important as a critical element in forming AlGaN epitaxial layers and is indispensable for 2D electron gas and

high electron mobility transistors (HEMTs). The structural isomorphism between AlN and GaN, along with the matching of their lattices and thermal expansions, allows for the creation of abrupt compositional junctions and the realization of desired functionalities. In the literature, the TBC of GaN and AlN has been of high interest for modeling studies;<sup>11,12</sup> however, experimental measurements have yet to be reported for a pristine GaN–AlN heterostructure, particularly for samples on a wafer scale.

Here, we introduce an optimized direct bonding approach to establish high-quality interfaces between GaN and AlN on a wafer scale. Our bonding process is illustrated in Fig. 1(a). The Ga-face of the GaN wafer was bonded to the N-face of the AlN wafer, using commercially available GaN (2 in., 400  $\mu$ m thick from Unipress) and AlN wafers (2 in., 400  $\mu$ m thick from Hexatech). The as-received Ga-face of the GaN substrates exhibited a roughness of less than 1 nm, while the N-face of the AlN substrates had an initial roughness of approximately 3 nm. We first conducted chemical-mechanical polishing to reduce the roughness of the AlN to less than 1 nm, based on our previous work with GaN.<sup>13</sup> Prior to bonding, the wafers were dipped into diluted HF: H<sub>2</sub>O solution to remove any surface oxides that existed on the wafer surfaces. After the HF treatment, the wafers were immediately submerged into a  $(NH_4)_2S$  solution for surface passivation. Afterward, the samples were rinsed, dried, and pressed under low applied pressure  $(\sim 50 \text{ kPa})$ . This process was based on our previous wafer bonding works with other III-V materials (GaAs, InP)<sup>13-16</sup> and the parameters described led to a well-bonded interface. As shown in the inset of Fig. 1(b), a significant fraction of the wafer surfaces was bonded, except for a couple of triangular regions associated with growth sector boundaries in the GaN. Subsequent annealing up to 800 °C was performed (GaAs and InP, for example, were annealed at  $200^{\circ}-400^{\circ}$  for 1-24 h<sup>14</sup>) to strengthen the bonding and to test the structure for high temperature stability. The similar thermal expansion coefficients and lattice constants between GaN and AlN<sup>17,18</sup> allows for the high-temperature annealing without debonding or cracking. To verify the structural quality, we performed high-resolution transmission electron microscopy (TEM) imaging on the GaN-AlN sample. Figure 1(b) shows the imaging results, revealing the atomically resolved interface. The crosssectional TEM image displays only a  $\sim$ 1.5 nm interfacial region, which is attributed to the possible structural reconfiguration of the interfaces (noting that this is approximately the sum of the pre-bonded surface

roughness layers) after a multi-step annealing process for the sample (at 350 °C for 22 h, 600 °C for 1 h, and 800 °C for 1 h). Other methods using surface activated bonding, plasma treatment, or other interfacial layers (SiN, Ti, etc.) typically results in an amorphous or oxide layer with a thickness of 2 nm or higher,<sup>19–21</sup> while in this work, a thinner and fully crystalline interface is observed. For thermal transport across the bonding interface, usually the foreign interlayer is desired to be thin to avoid possible additional resistance.<sup>22,23</sup> Noteworthy to mention, for this reported processing, our wafer bonding procedure was carried out at room temperature, ambient atmosphere, and at low bonding pressures (compared to typical bonding forces on the MPa scale), signifying the simplicity of the process.

We performed direct measurements of the TBC between the GaN-AlN interfaces using the time-domain thermoreflectance (TDTR) method. TDTR is an established thermal technique for providing reliable characterizations of interface thermal conductance, and we have utilized it for measurements across a wide range of materials,5-7,24-29 including heterostructures of GaN with high thermal conductivity boron arsenide (BAs) cooling substrates.<sup>5</sup> To enhance the measurement sensitivity for the bonded GaN-AlN samples, we prepared GaN samples with controlled thickness by grinding and polishing the GaN film to less than 500 nm, as illustrated in Fig. 2(a). During TDTR measurements, a thin aluminum film was deposited on the sample to act as a transducer for converting pump laser energy into thermal excitation and as a sensor for temperature detection. The basic operation of TDTR is schematically represented in Fig. 2(b): the incident pump pulse causes an instantaneous temperature rise with a  ${\sim}75$ fs pulse width, inducing a detectable temperature change by the probe beam. The arrival time difference between the pump and probe pulses is controlled with sub-picosecond resolution using a mechanical delay stage. The collected data are then analyzed using a multilayer thermal model to determine TBC and thermal conductivity ( $\kappa$ ). More details about the experimental setup and modeling analysis can be found in our previous publications.<sup>26</sup>

To ensure measurement accuracy, we conducted calibration experiments and sensitivity analysis. Calibration experiments were carried out on GaN and AlN samples to ascertain the properties of the heterostructure, including those of the AlN and Al-GaN interfaces. We performed TDTR measurements on these wafers, determining the thermal conductivity of AlN to be  $329 \pm 29$  W/mK and the thermal

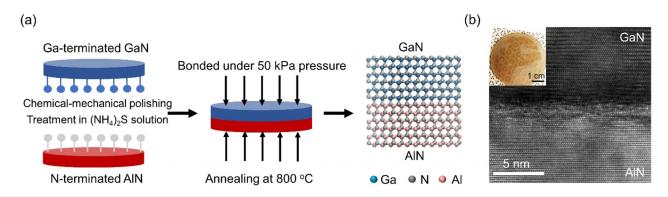


FIG. 1. Fabrication procedure for our wafer-scale bonding process and the characterization of GaN–AIN interfaces. (a) Schematic diagram of the bonding process between GaN and AIN wafers. (b) High-resolution transmission electron microscope (TEM) image of the GaN–AIN interface. Inset, optical image of a wafer-scale sample during bonding.

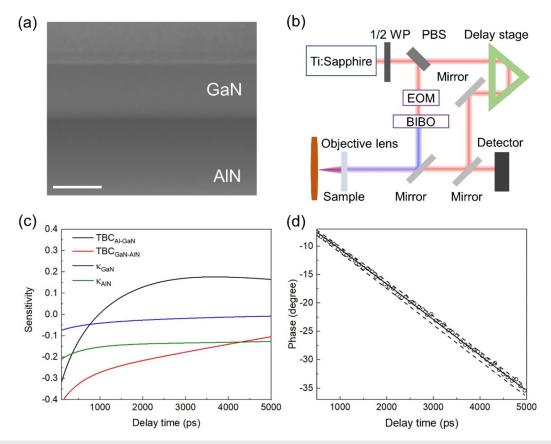


FIG. 2. Thermal transport experiments and measurement sensitivity modeling for GaN–AIN interface structures. (a) A cross-sectional scanning electron microscope image of a GaN–AIN interface sample. Scale bar: 200 nm. (b) A schematic of the time-domain thermoreflectance (TDTR) experimental setup. WP: waveplate; PBS: polarizing beam splitter; EOM: electro-optic modulator; BIBO: bismuth borate. (c) Sensitivity analysis and thermal modeling for the experimental structure of Al–GaN–AIN. The GaN layer is 200 nm thick for the plot. (d) Experimental data from TDTR measurements (circles) are plotted along with the best fit (solid line), compared with  $\pm$ 10% variation in thermal conductance (dashed lines).

boundary conductance (TBC) of Al-GaN to be  $87 \pm 7 \text{ MW/m}^2\text{K}$  at room temperature. Utilizing the calibrated values, we conducted a modeling analysis to ensure high measurement sensitivity to the thermal conductance of GaN–AlN. The measurement sensitivity is quantified according to the standard definition,  $^{27,30,31}$   $S_{\alpha} = \frac{\partial \ln(\theta)}{\partial \ln(\alpha)}$ , which represents the change in the measurement signal induced by the variation of an interested parameter. Sensitivity analysis was performed for various parameters, such as TBC<sub>Al-GaN</sub>,  $\kappa_{GaN}$ , TBC<sub>GaN-AlN</sub>, and  $\kappa_{AlN}$ , and GaN thicknesses ranging from 150 to 500 nm. The sample structures and thicknesses are thus optimized to achieve high measurement sensitivity to TBC<sub>GaN-AlN</sub>, as exemplified in Fig. 2(c). Consequently, we prepared the heterostructure samples with GaN thickness within this range. Figure 2(d) presents example TDTR measurement data alongside the model fit and a  $\pm 10\%$  variation of thermal conductance, offering a reliable determination of thermal conductance.

The thermal conductance of GaN–AlN interfaces, measured with temperature dependence, is presented in Fig. 3(a). The TBC value increases from 77 MW/m<sup>2</sup>K at 80 K to 320 MW/m<sup>2</sup>K at 300 K, indicating typical crystalline behavior.<sup>4,5,26,32,33</sup> By comparison, the measured TBC of GaN–AlN in this study is among the highest compared to

other GaN–AlN–X (X: SiC, Si, or diamond) interfaces. It should be noted that in the literature, AlN is usually used as a buffer layer deposited between GaN and other materials with the aim of enhancing TBC. For instance, at room temperature, TBC values reported in the literature typically fall below 50 W/m<sup>2</sup>K for GaN–diamond interfaces due to intrinsic phonon mismatch and defects<sup>34,35</sup> and increases to  $60-100 \text{ W/m}^2\text{K}$  for GaN–AlN–diamond;<sup>36,37</sup> for GaN–silicon or GaN–SiC interfaces, using an AlN buffer layer could improve TBC up to 200 MW/m<sup>2</sup>K due to better phonon spectral matching.<sup>32,38–40</sup> Here, the measured high TBC of up to 320 MW/m<sup>2</sup>K for GaN–AlN indicates that the reported interfaces involving an AlN buffer layer may be limited by defects-induced thermal resistance rather than intrinsic resistance. Meanwhile, we also note that the intrinsic TBC of heterostructure interfaces is fundamentally limited by phonon spectral matching.<sup>4,5</sup>

To model the TBC of the GaN–AlN interface, we applied the non-equilibrium Green's function method, utilizing accurate force constants derived from density functional theory (DFT) calculations. Figure 3(b) presents a schematic of the interface structure used in our modeling, where AlN was strained to match the lattice constants of 26 January 2025 21:38:07

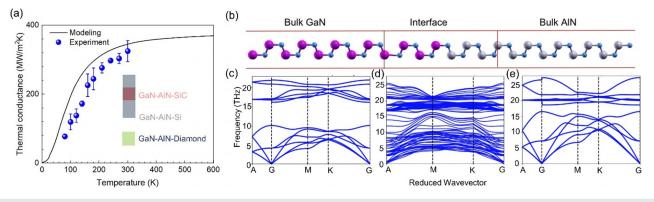


FIG. 3. Experimental measurement and atomistic Green's functions modeling results for thermal boundary conductance (TBC) of the GaN–AIN interface. (a) Temperaturedependent TBC from measurements and calculations with atomic Green's functions. Also plotted are literature reports on GaN interfaces with an AIN buffer layer. (b) A schematic of the GaN–AIN atomic interfaces in density functional theory calculations, where AIN is strained to match the lattice constants of GaN and form bonds between AI and N atoms at the interface. (c)–(e) Calculated phonon dispersions for GaN, the supercell of GaN–AIN interface and AIN.

GaN, forming an abrupt interface created by bonding Al atoms with N atoms. The DFT approach used a plane wave basis set as implemented in the Quantum Espresso suite of codes as in prior work,<sup>41</sup> using Rappe-Rabe-Kaxiras-Joannopoulos ultrasoft pseudopotentials. The generalized gradient approximation with the Perdew-Burke-Ernzerhof functional form approximates the exchange correlation energy. Three sets of first-principles calculations were performed for the results reported here, involving calculations on bulk GaN, bulk AlN, and a GaN-AlN interface supercell. Structural relaxation minimized Hellmann-Feynman forces below 10<sup>-3</sup> eV/Å. Density functional perturbation theory was used to extract phonon dynamics, with dynamical matrices extracted from  $4 \times 4 \times 3$  q-point grids in the bulk materials and a  $4 \times 4 \times 1$  q-point grid for the interface supercell. A Fourier transform of the dynamical matrices allows the extraction of interatomic force constants used in Green's function transport calculations and phonon dispersion curves for the three sets of interest, as depicted in Figs. 3(c)-3(e). The good overlap between phonon dispersions of GaN and AlN, together with the broaden phonon channels by interface modes, leads to inherent high TBC. By applying a 10 K temperature difference across the two bulk contacts and solving the non-equilibrium Green's function, we computed Buttiker probe temperatures.42 The TBC was calculated from the heat flux across the interface and the temperature drop at the interface. The calculated TBC is  $\sim$ 340 MW/m<sup>2</sup>K when only elastic scattering is considered, close to our measurement result. The consistence between experimental and modeling results regarding the temperature dependence also implies the high quality of the GaN-AlN interface. By contrast, strong phonon-defect scatterings are typically insensitive to temperature variations and can significantly reduce the TBC.<sup>4,43–45</sup>

In addition, we observed a substantial size effect on the TBC by examining samples with varied GaN thicknesses, ranging from 180 to 450 nm. As shown in Fig. 4(a), the experimentally measured TBC increases from ~190 MW/m<sup>2</sup>K to reach around 320 MW/m<sup>2</sup>K as the GaN thickness increases from 180 to 450 nm. To understand the size effects on TBC, we performed molecular dynamics (MD) simulations with varied sizes of the simulation domains. We employed nonequilibrium MD simulations, utilizing the methodology detailed in our previous publications. <sup>5,6,26</sup> The interfaces were created by joining two slabs of GaN and AlN, each with a cross-sectional area of  $5 \times 5 \text{ nm}^2$  and periodic boundary condition. The Stillinger–Weber potential was used for both materials.<sup>46,47</sup> Periodic boundary conditions were applied on all sides. Initially, the systems were equilibrated under isothermal– isobaric ensemble conditions (300 K and 0 Pa) for 5 ns, followed by a 1 ns relaxation period under canonical ensemble conditions at 300 K. Subsequently, a microcanonical ensemble was maintained for 1 ns. The two ends of the system were frozen and fixed, with their adjacent layers functioning as the heat source and sink, with a constant heat flux introduced across the interface. The temperature drop  $\Delta T$  at the interface was then sampled after reaching a steady state and used to calculate TBC according to  $G = Q/\Delta T$ , where Q represents the heat flux per area.

The size effects of TBC are evaluated from the comparison between the experimental and MD simulation results in Fig. 4(a), showing a consistent increase in TBC as the GaN thickness increases. To examine phonon spectral contributions, we calculated the spectral thermal conductance by applying a Fourier transform to the autocorrelation function of the heat current at the interface.<sup>48</sup> As presented in Fig. 4(b), the TBC was decomposed into contributions from a spectrum of phonon modes. From the figure, the interface thermal transport and TBC are predominantly influenced by the phonons with frequencies below 10 THz, which also exhibit significant size dependence. By comparison, higher-frequency phonons (above 10 THz) contribute less to the overall TBC and show negligible size effects. This MD result indicates that the reduction in thermal conductance primarily originates from the suppression of lower-frequency phonons, which have longer mean free paths, and their quasi-ballistic thermal transport.<sup>24,28,29</sup> The thickness confinement of the GaN layer suppresses the availability of the phonon modes near the interface. We note that this conclusion is consistent with the modeling results by Liang *et al.*<sup>49</sup> and Wang et al.<sup>50</sup>

In summary, we have developed a wafer-scale bonding process and reported the experimental measurement of GaN–AlN interfaces. The interface thermal boundary conductance measured from the experiment reaches up to  $320 \text{ MW/m}^2$ K. The exhibited strong temperature dependence is attributed to weak scattering by defects in crystals and better approaching the intrinsic limit. The atomic Green's function method, utilizing force constants derived from density functional theory, was adopted to account for the phonon spectra and

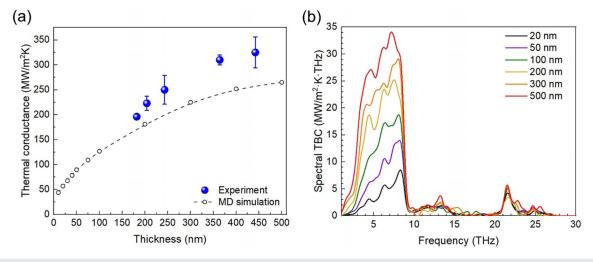


FIG. 4. Size effect on thermal boundary conductance (TBC) from experiments and molecular dynamics. (a) Measurement results of TBC dependent on GaN thickness, plotted alongside molecular dynamics simulation results. (b) Spectral thermal boundary conductance of GaN–AIN interfaces with varied thicknesses of the GaN layers.

calculate the TBC, showing consistency with the experimental results. Additionally, size effects on TBC, dependent on GaN thickness, were experimentally observed and analyzed through molecular dynamics simulations. The size-dependent TBC is attributed to quasi-ballistic thermal transport and the suppression of phonon scattering from long mean free path modes due to thickness confinement. This study has developed a scalable route for manufacturing GaN heterostructures with high TBC interfaces. Further exploration of this bonding approach with materials of ultrahigh thermal conductivity, such as boron arsenide (BAs),<sup>5,24,51</sup> could overcome current thermal budget limitations and significantly improve the heat dissipation and device performance of wide and ultrawide bandgap semiconductors. The combined approach of experimental measurements and modeling analysis, along with electrical studies, could enable advanced thermal management technologies based on electrical-thermal-mechanical codesign strategies.

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## AUTHOR DECLARATIONS

#### **Conflict of Interest**

The authors have no conflicts to disclose.

#### **Author Contributions**

Man Li, Kaicheng Pan, and Yijun Ge contributed equally to this work.

Man Li: Conceptualization (equal); Data curation (lead); Formal analysis (lead); Investigation (equal); Methodology (equal); Validation (lead); Visualization (lead); Writing – original draft (lead); Writing – review & editing (equal). Kaicheng Pan: Data curation (equal); Formal analysis (equal); Methodology (equal); Validation (equal); Visualization (equal); Writing – review & editing (equal). Yijun Ge: Data curation (equal); Formal analysis (equal); Investigation (equal). **Kenny Huynh:** Data curation (equal); Investigation (equal); Methodology (equal). **Mark S. Goorsky:** Conceptualization (equal); Funding acquisition (equal); Investigation (equal); Methodology (equal); Project administration (equal); Resources (equal); Supervision (equal); Writing – review & editing (equal). **Timothy S. Fisher:** Conceptualization (equal); Funding acquisition (equal); Investigation (equal); Project administration (equal); Resources (equal); Supervision (equal); Writing – review & editing (equal). **Yongjie Hu:** Conceptualization (equal); Funding acquisition (equal); Investigation (equal); Project administration (equal); Resources (equal); Supervision (equal); Project administration (equal); Writing – review & editing (equal); Writing – original draft (equal); Writing – review & editing (equal).

#### DATA AVAILABILITY

The data that support the findings of this study are available within the article.

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